

MEMORY ARRAY

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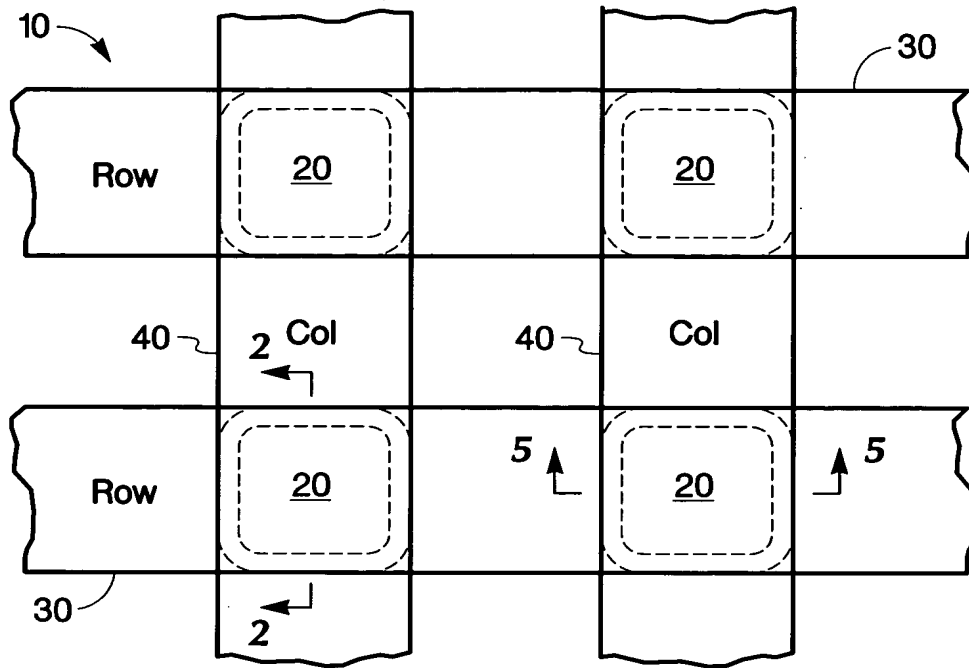


Fig. 1

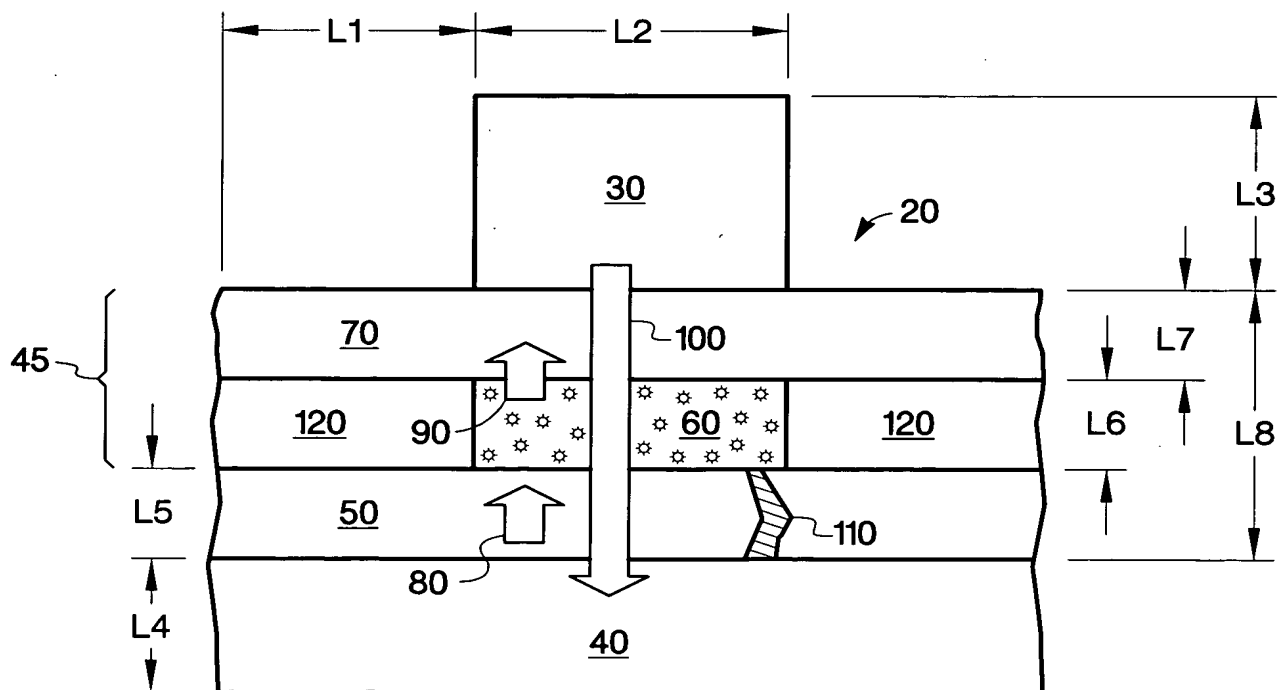


Fig. 2

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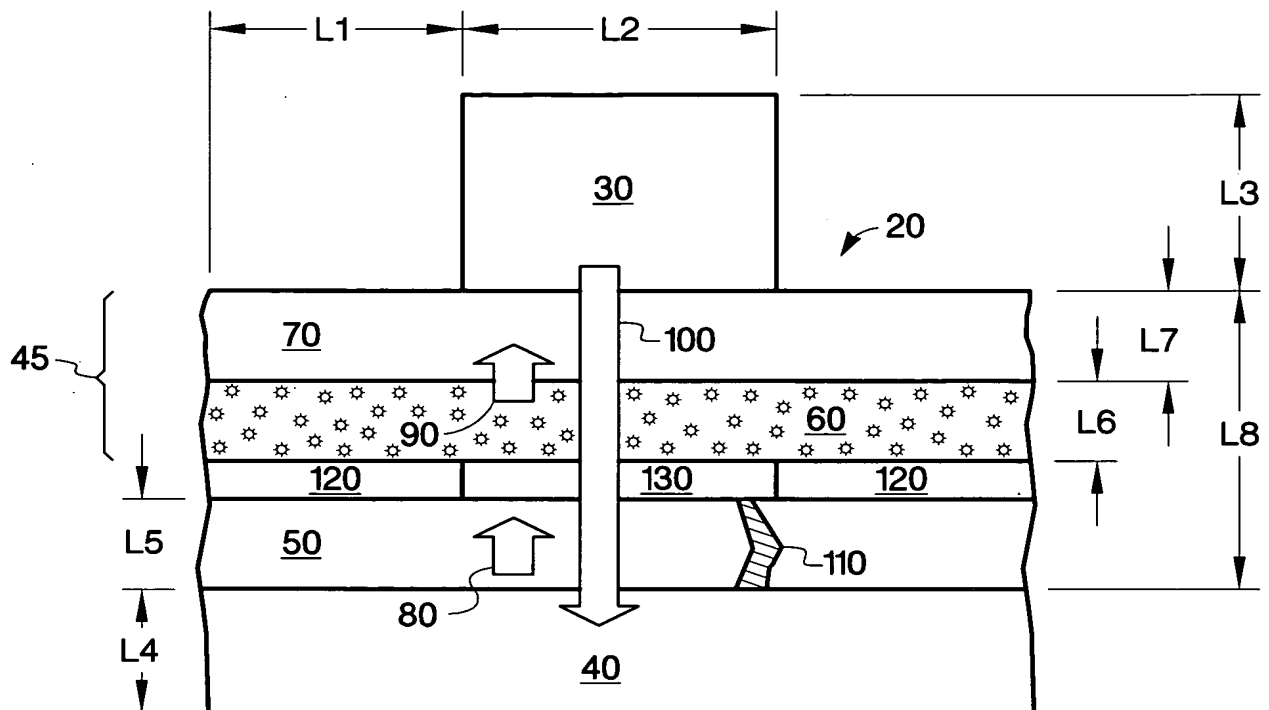


Fig. 3

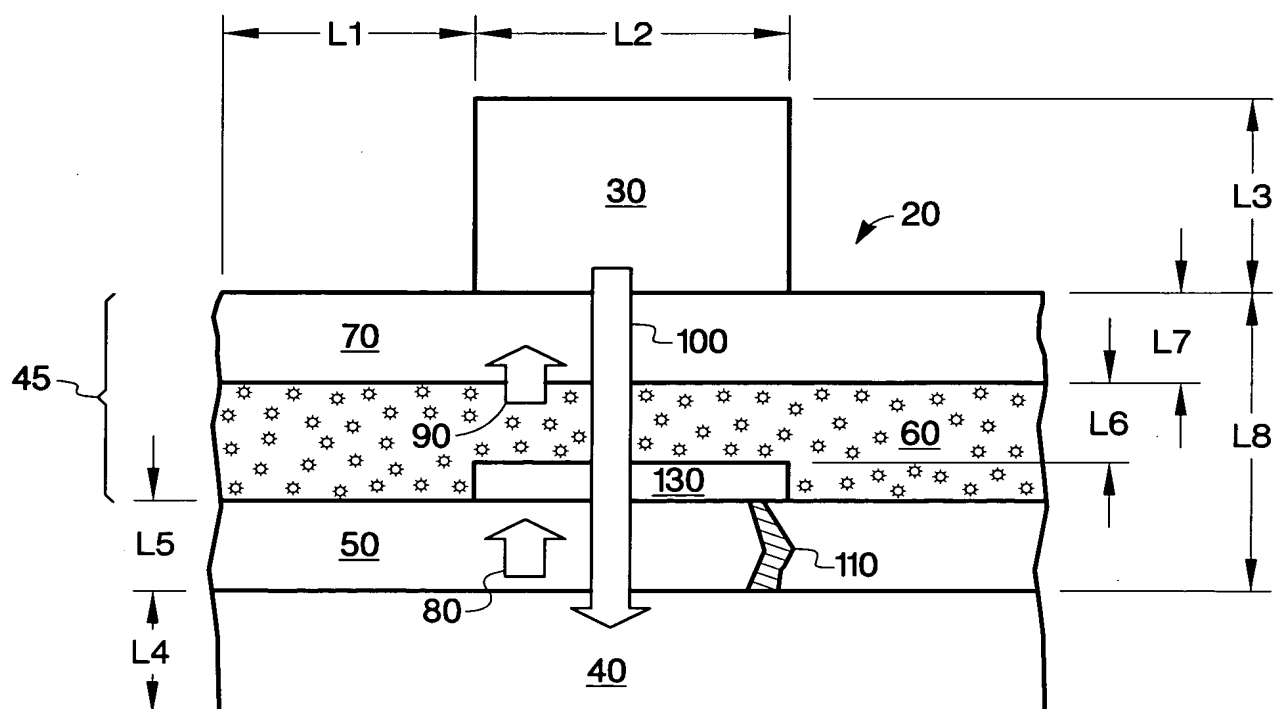


Fig. 4

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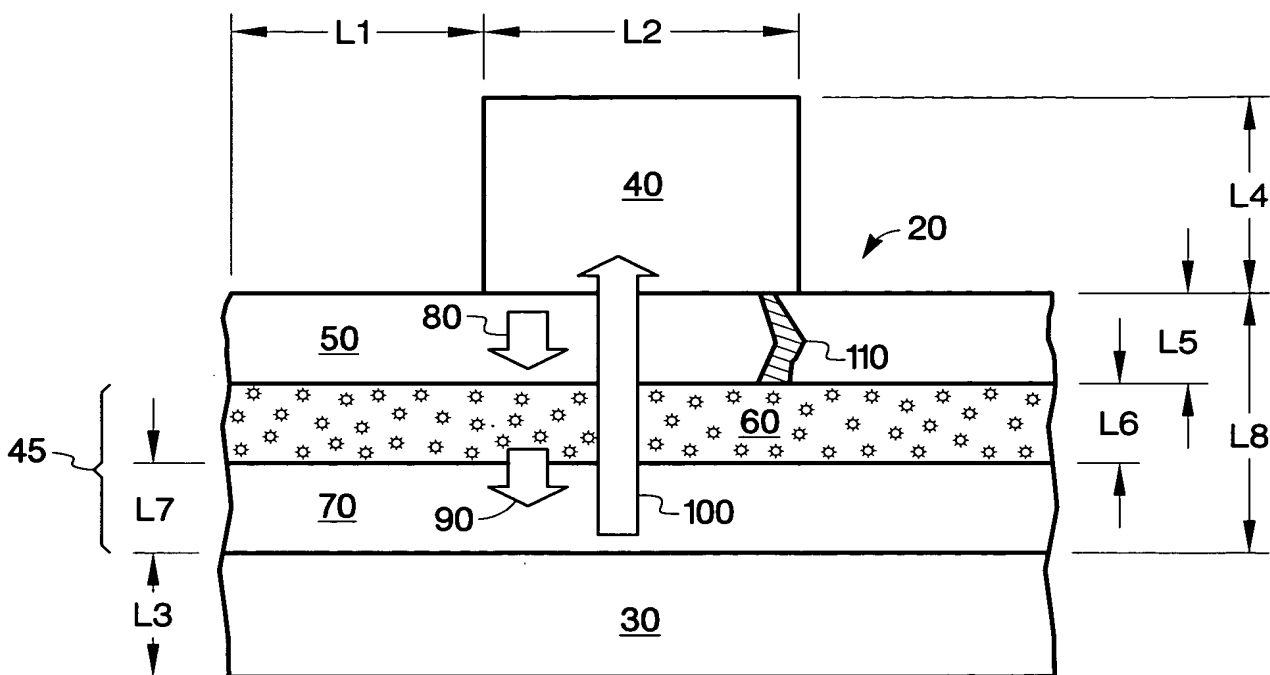


Fig. 5

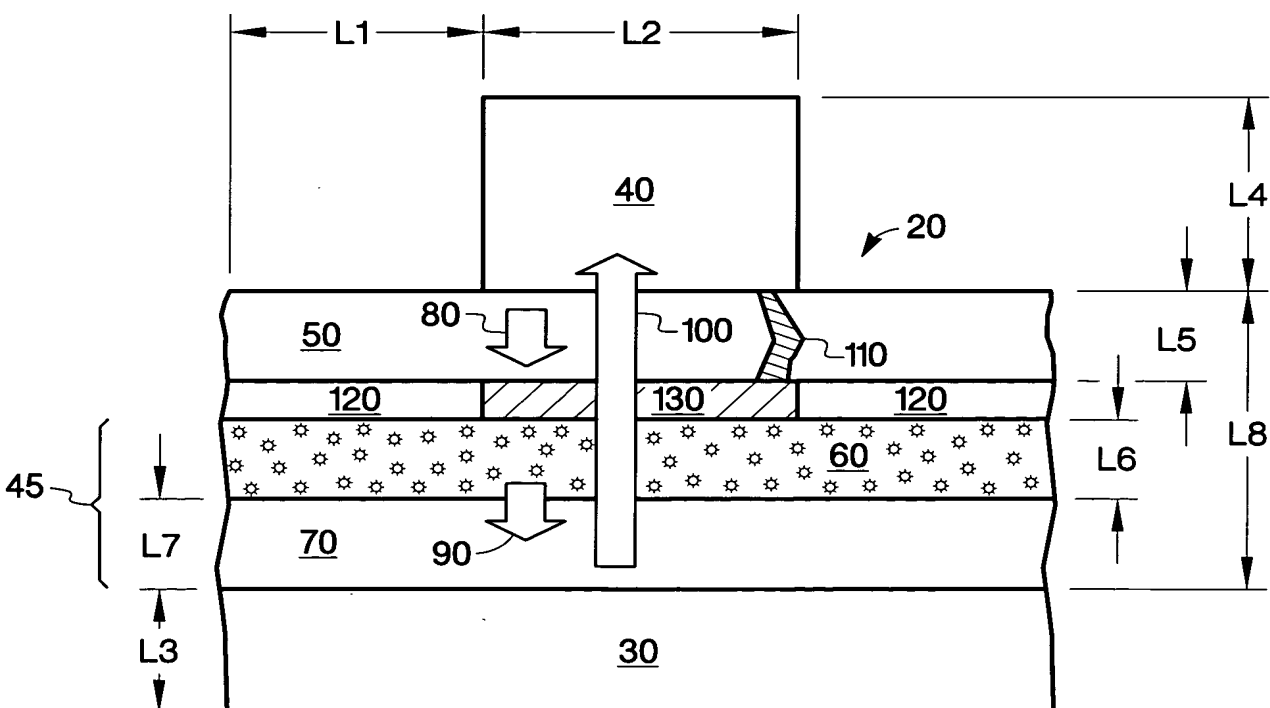


Fig. 6

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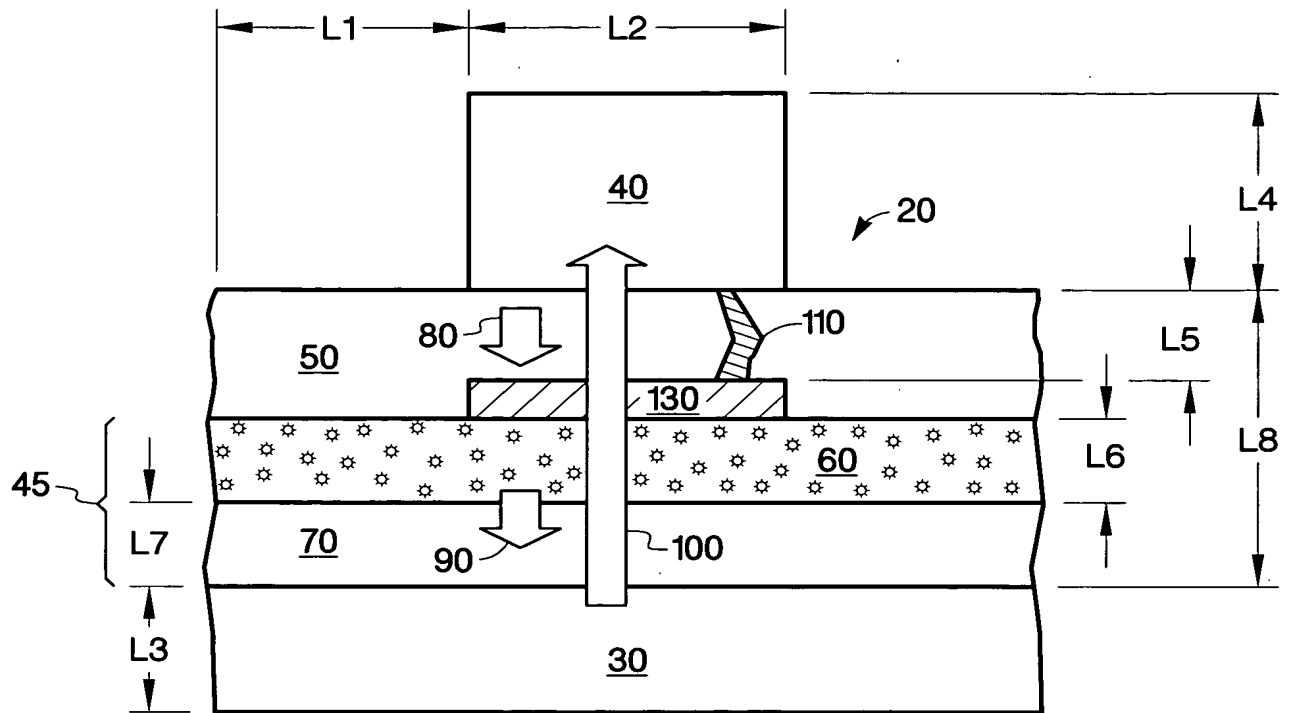


Fig. 7

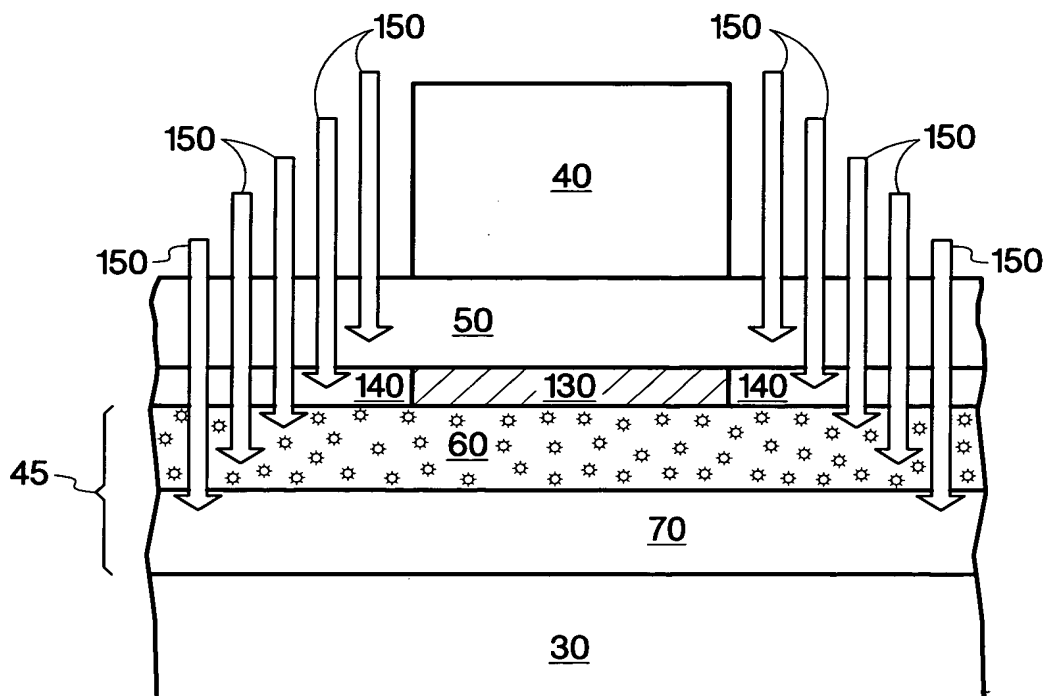


Fig. 8

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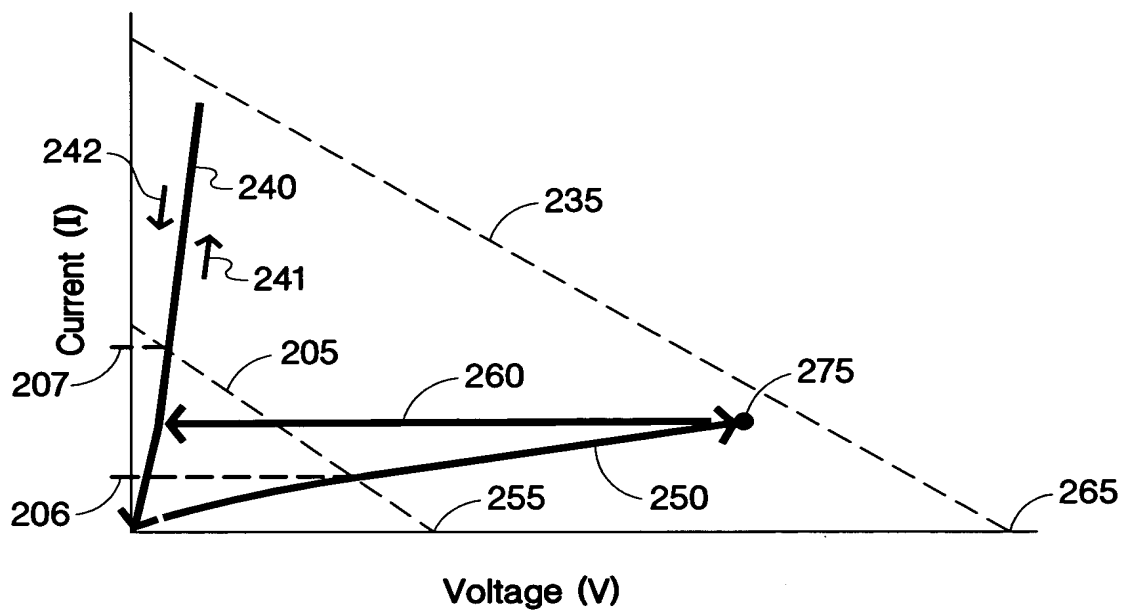


Fig. 9

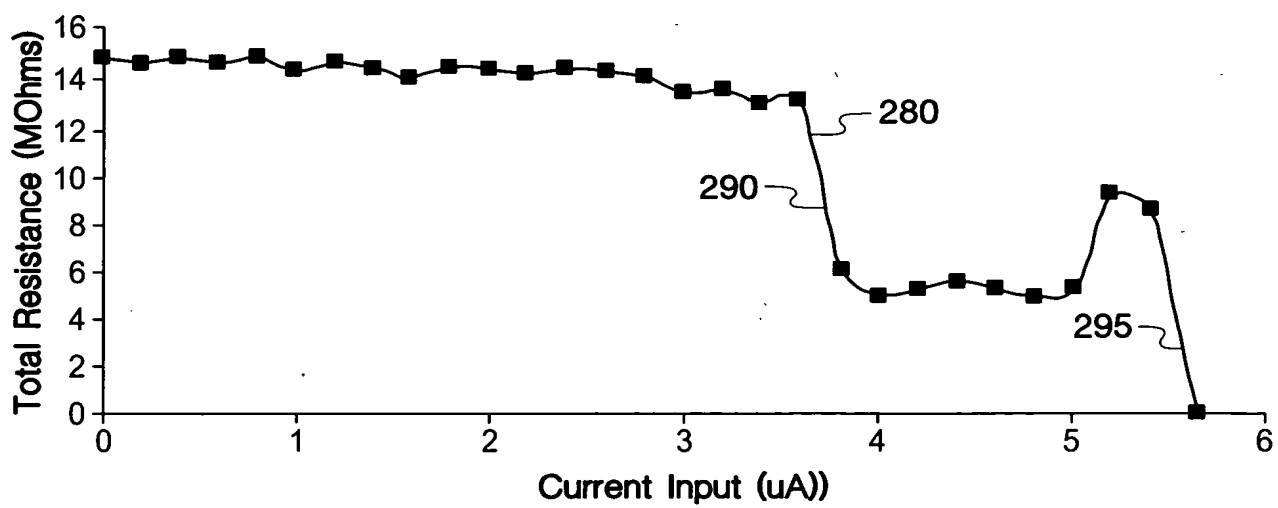


Fig. 10

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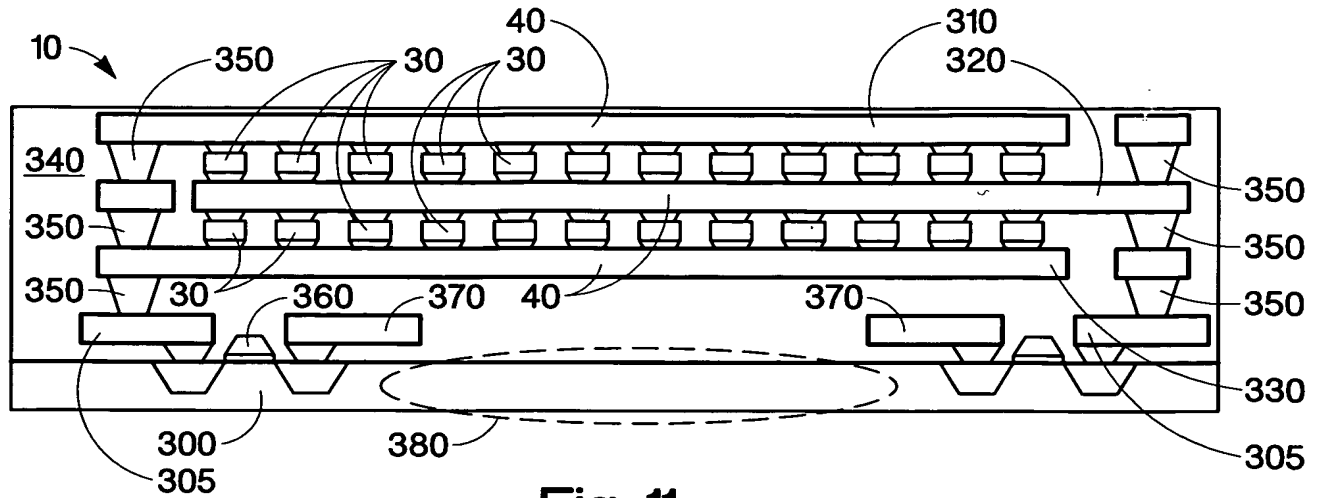


Fig. 11

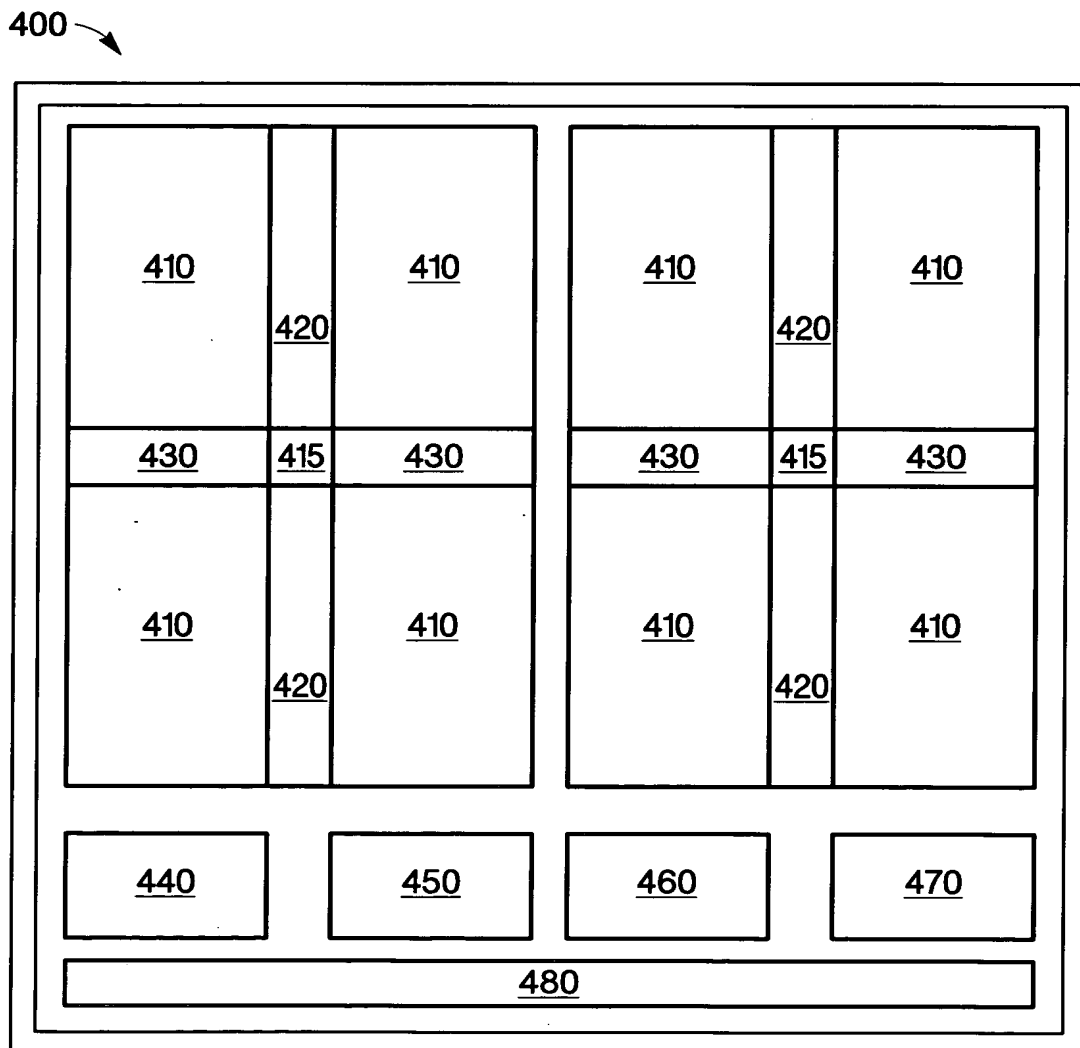


Fig. 12

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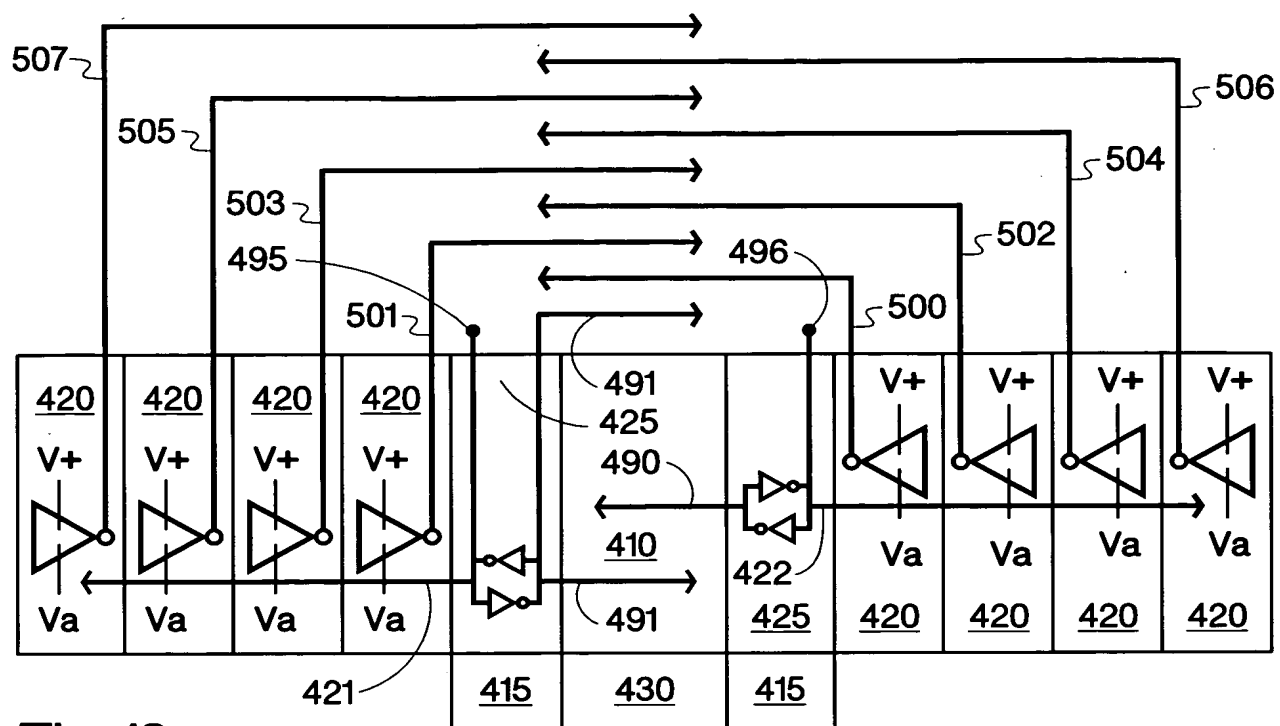


Fig. 13

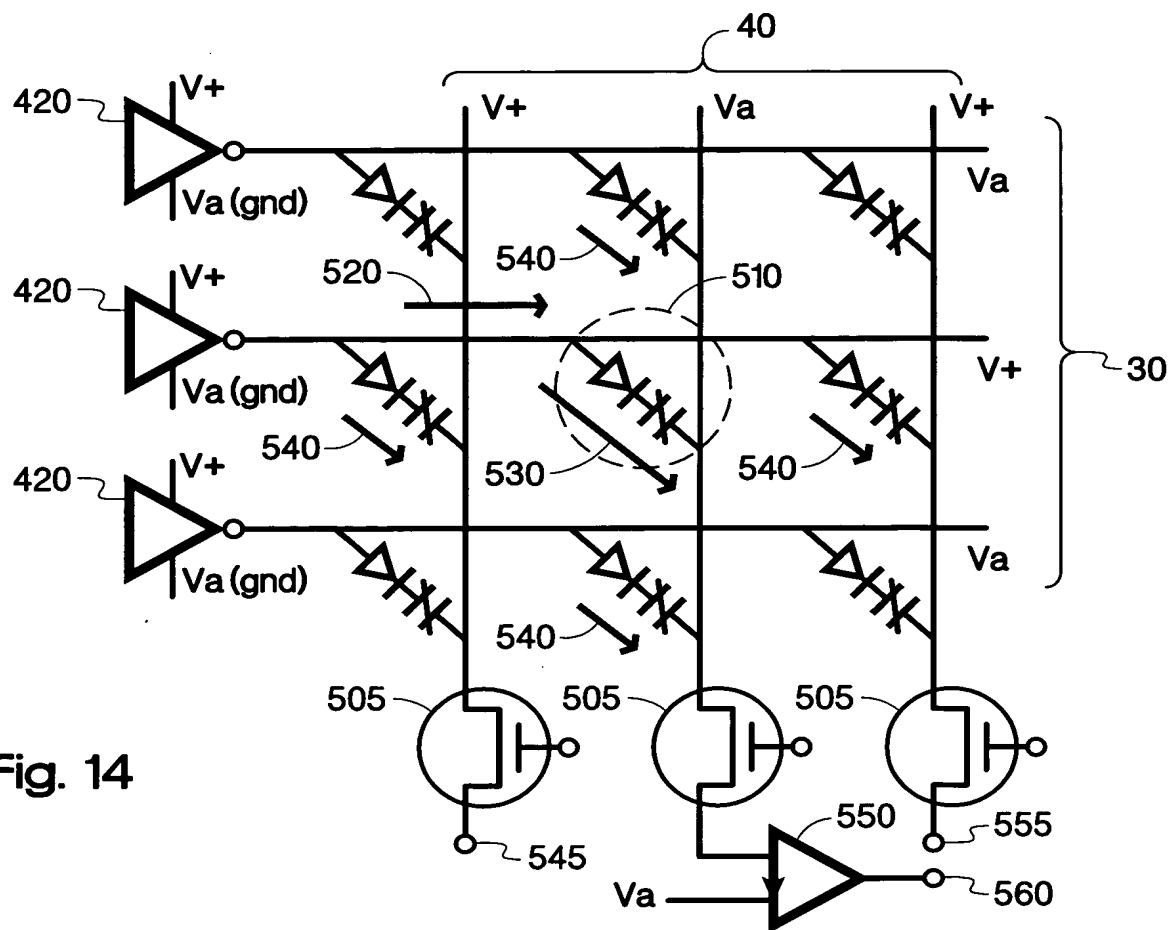


Fig. 14

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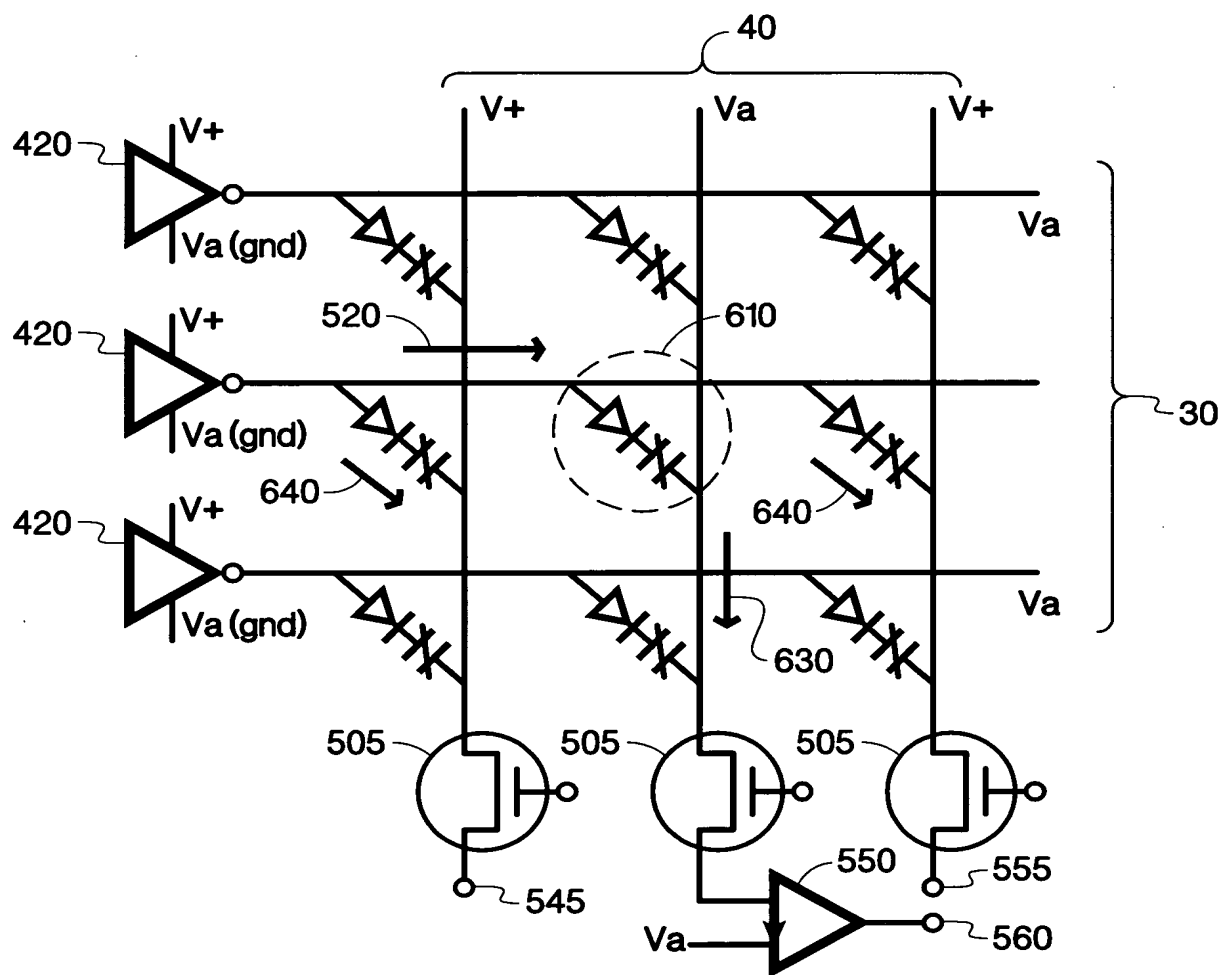


Fig. 15



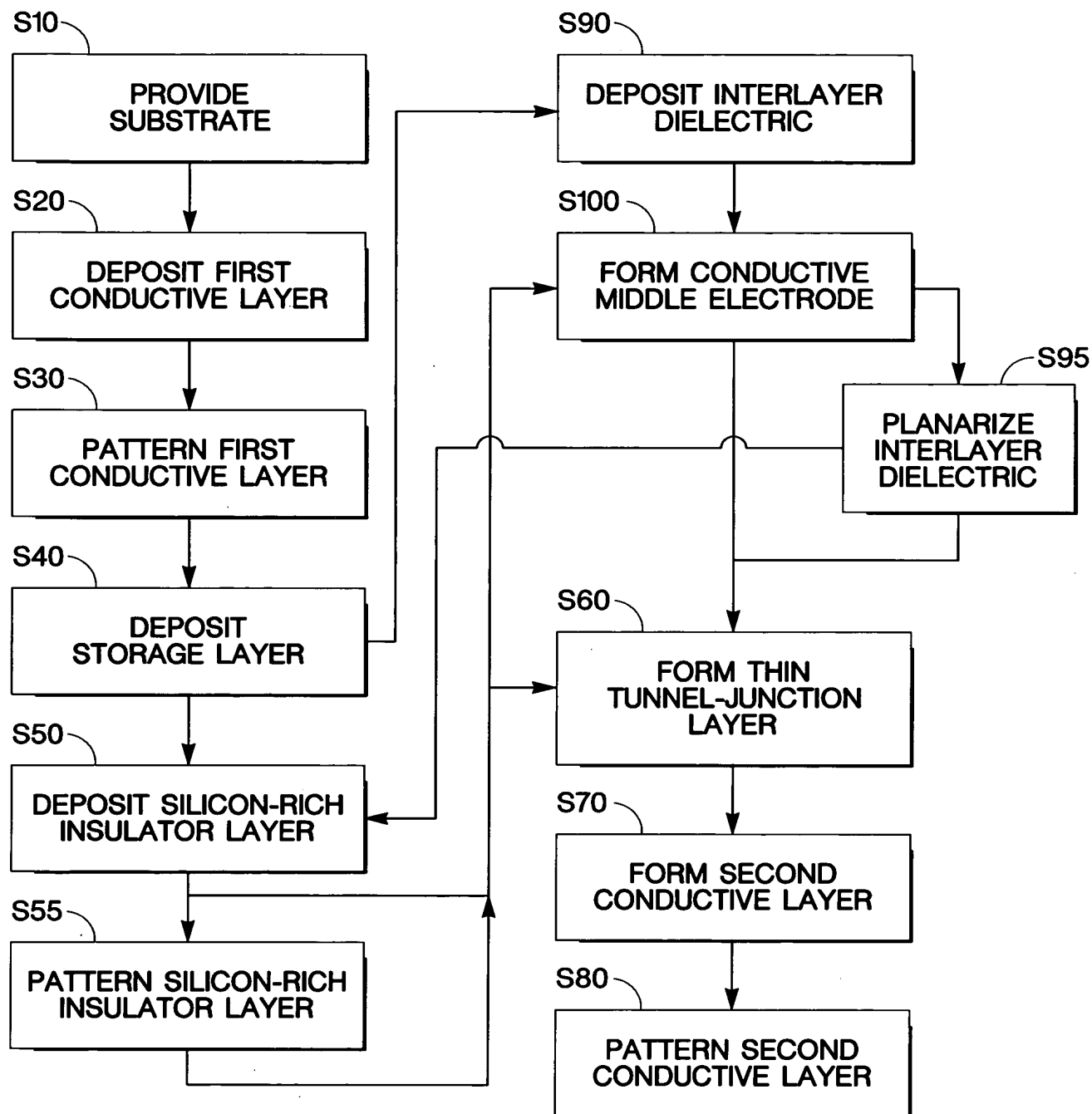


Fig. 16